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Title: MICRORELAYS AND MICRORELAY FABRICATION AND OPERATING METHODS

1st Named Inventor: Uppili Sridhar

Application No.: 10/645,993

Sheet: 1 of 4

Docket No.: 55123P230D

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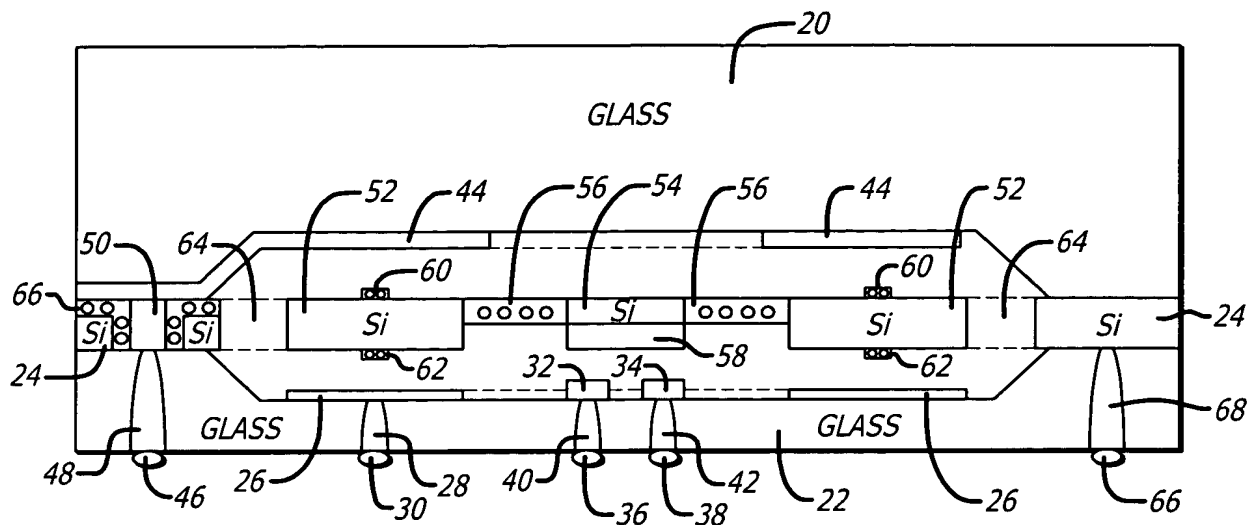


FIG. 1

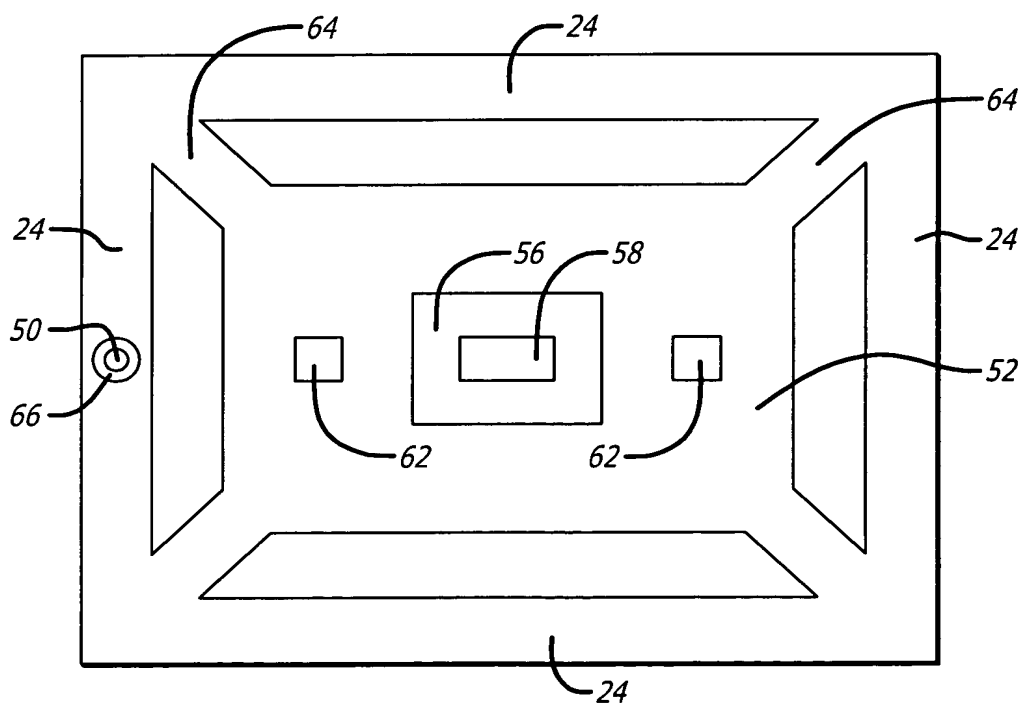


FIG. 2

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FIG. 3D

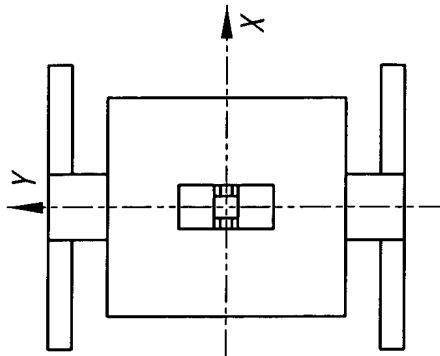


FIG. 3C

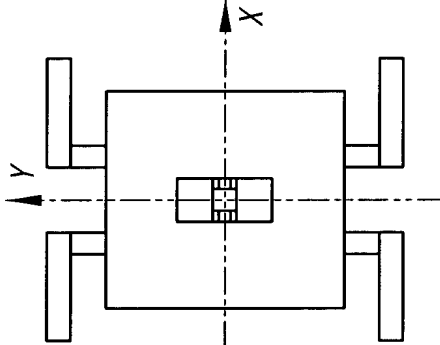


FIG. 3B

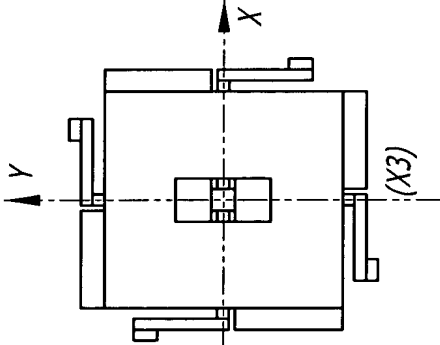


FIG. 3A

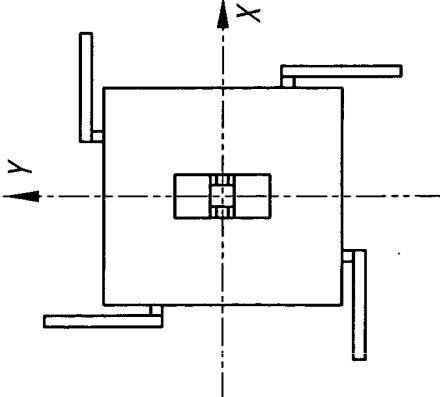


FIG. 3G

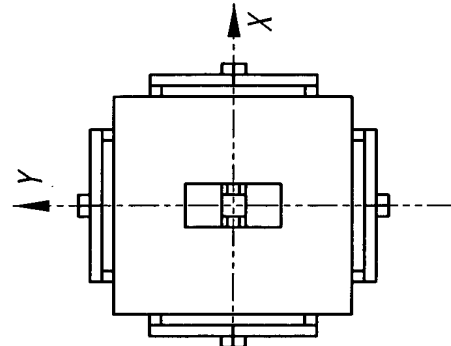


FIG. 3F

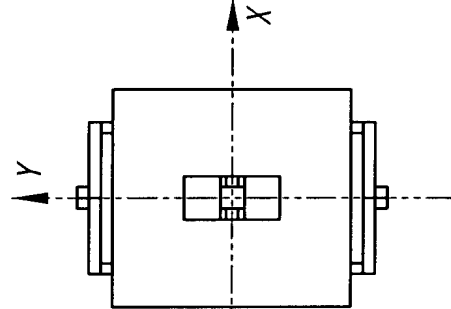
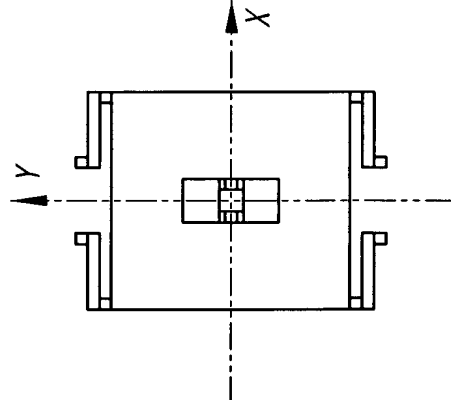


FIG. 3E



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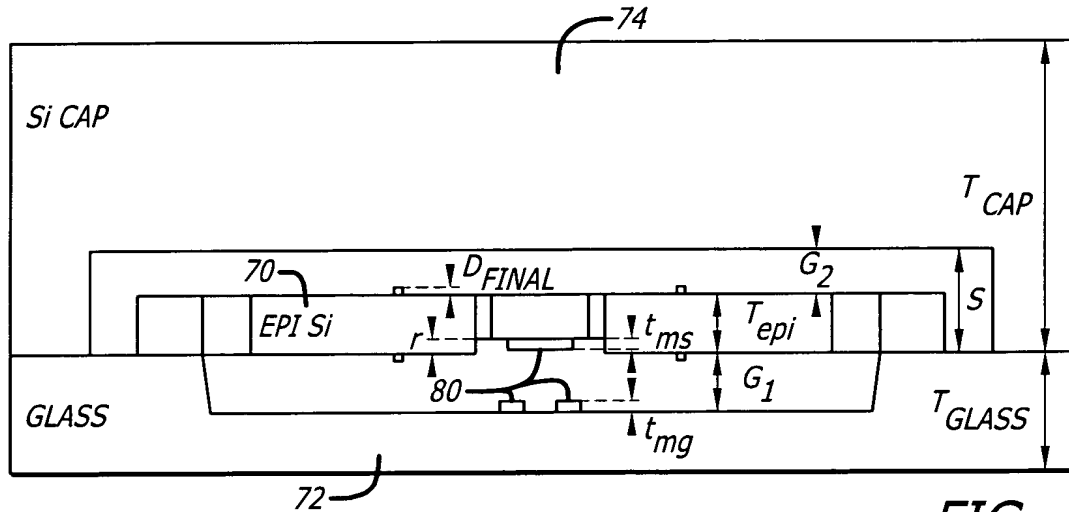
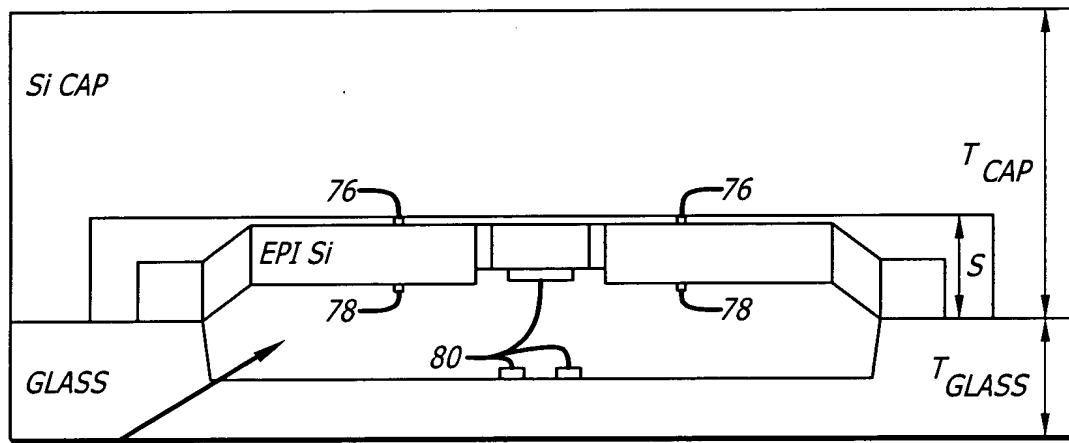


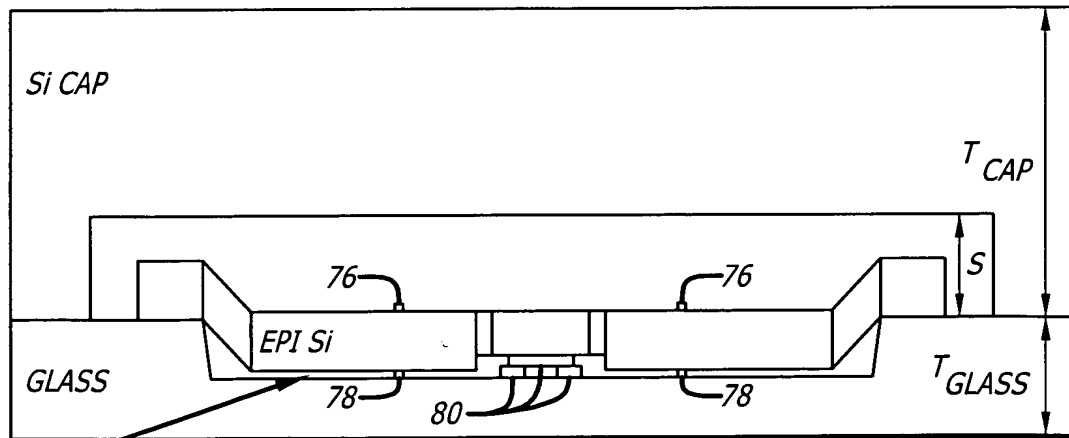
FIG. 4



OFF-STATE GAP

OFF-STATE

FIG. 5



ON-STATE GAP

ON-STATE

FIG. 6

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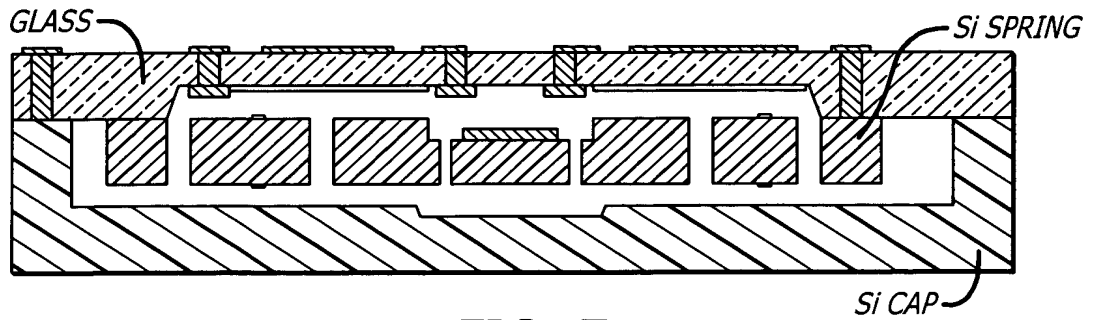


FIG. 7

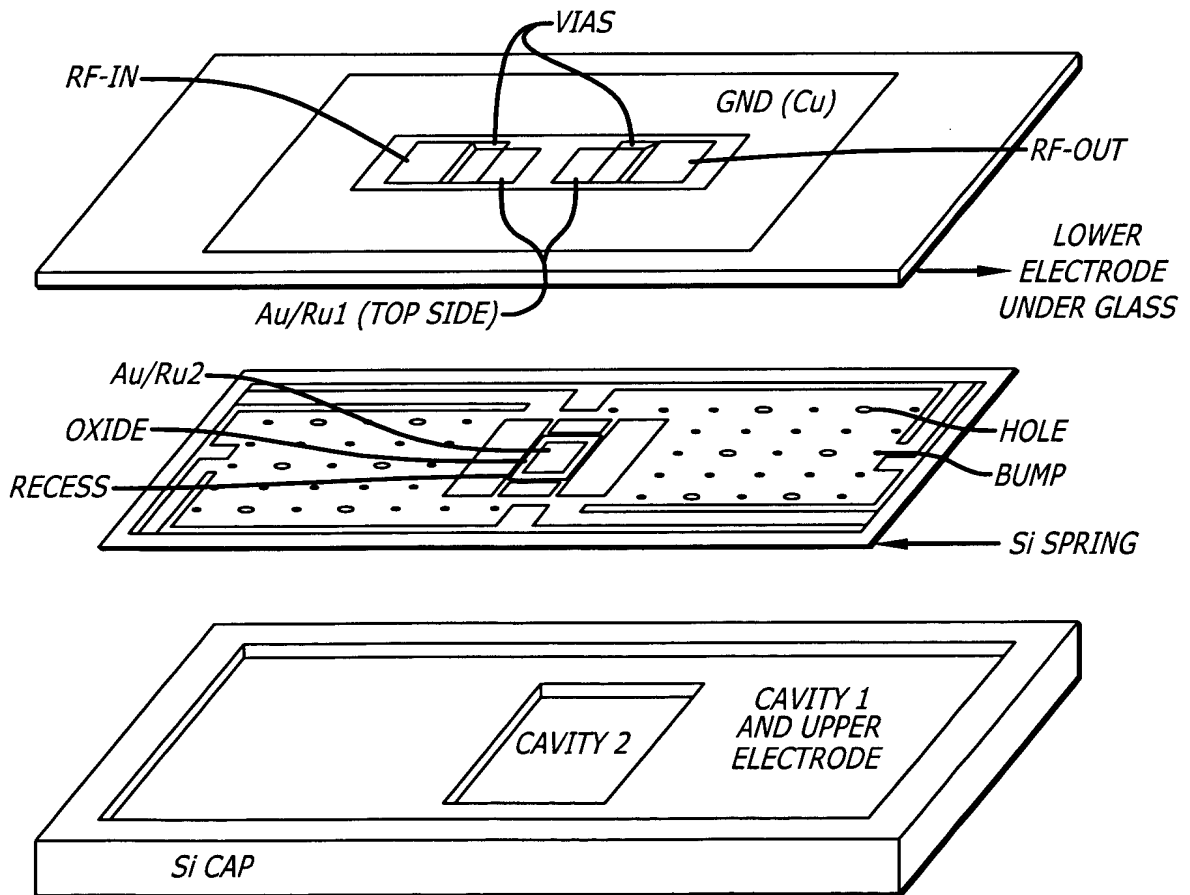


FIG. 8